

IN THE SPECIFICATION:

Please amend paragraph number [0005] as follows:

[0005] As electrons are emitted by emitter tips and accelerate past the row line that extends over the pixel, the electrons are directed toward a corresponding pixel of a positively charged electro-luminescent panel of the field emission display, which is spaced apart from and substantially parallel to the field emission array. As electrons impact a pixel of the ~~electro-luminescent~~ electro-luminescent panel, the pixel is illuminated. The degree to which the pixel is illuminated depends upon the number of electrons that impact the pixel.

Please amend paragraph number [0009] as follows:

[0009] The present invention includes a method of fabricating the pixels of a field emission array and, in particular, defining emitter tips and their corresponding resistors by employing a single mask. The field emission array fabrication method of the present invention may also include electrically isolating adjacent column lines from one another ~~with~~ without requiring the use of an additional mask. Field emission arrays fabricated in accordance with the inventive method are also within the scope of the present invention.

Please amend paragraph number [0027] as follows:

[0027] FIG. 8 is a schematic cross-sectional representation of the field emission array of FIG. 7, with the hard mask removed therefrom and illustrating planarization of the emitter ~~tip-resistor~~ tip-resistor layer;

Please amend paragraph number [0041] as follows:

[0041] FIG. 7 illustrates the removal of portions of emitter tip-resistor layer 24 that are exposed through hard mask 44 and the exposure of at least substantially longitudinal center portions 34 of conductive lines 22 through emitter tip-resistor layer 24. Preferably, the removal of these substantially longitudinal center portions 34 of emitter tip-resistor layer 24 is substantially anisotropic. The material or materials of the exposed portions of emitter tip-resistor

layer 24 may be removed by known processes, such as by the use of etchants that are selective for one or more materials of emitter tip-resistor layer 24 over the mask material of hard mask 44. Preferably, at least a peripheral lateral edge portion 36 of selected ~~column~~ conductive lines 22 remains covered by emitter tip-resistor layer 24 so as to facilitate the subsequent removal of only a portion of the selected conductive lines 22.